**PATENT** 

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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In resophication of: Li, Weimin; Yin, Zhiping; and Budge, William

Application No.: 09/536,037

Group No.: Unknown

Filed:

0 3 2000

03/27/2000

Examiner: Unknown

For: Low k Interlevel Dielectric Layer Fabrication Methods

Assistant Commissioner for Patents

Washington, D.C. 20231



### REQUEST FOR CORRECTED FILING RECEIPT

- 1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested. Also attached is a true copy of the title page for your reference.
- 2. There is an error with respect to the following, which is incorrectly entered.

Error in

Correct data

1. Title

1. Low k Interlevel Dielectric Layer Fabrication Methods

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Date: July 3

(Request for Corrected Filing Receipt—page 1 of 2)

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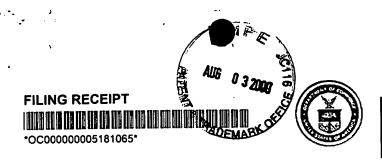
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS	
09/536,037	03/27/2000	2825	708	MI22-1398	1	21	2	

021567 WELLS ST JOHN ROBERTS GREGORY AND MATKIN SUITE 1300 601 W FIRST AVENUE SPOKANE, WA 992013828

Date Mailed: 06/16/2000

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#### Applicant(s)

Weimin Li, Boise, ID; Zhiping Yin, Boise, ID; William Budge, Homedale, ID;

**Continuing Data as Claimed by Applicant** 

THIS APPLICATION IS A DIV OF 09/388,826 09/01/1999

Foreign Applications

If Required, Foreign Filing License Granted 06/15/2000

Title

Low k interlevel dielectic layer fabrication methods

**Preliminary Class** 

438

Data entry by: NEWTON, WILLIAM

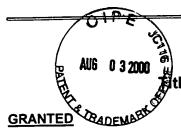
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